

SYSTEM AND METHOD FOR RECOVERING FROM MEMORY ERRORS

ABSTRACT

A system with capabilities for recovering from memory errors includes memory and a processor in communication with the memory. The system also includes
5 an operating system and a BIOS which reside in the memory and are executable by the processor. The BIOS includes recovery logic that detects a memory error in a section of the memory and, in response, instructs the operating system to discontinue use of the section of memory with
10 the memory error. For instance, the system may include multiple memory objects that represent respective sections of the memory. The recovery logic may instruct the operating system to discontinue use of the section of memory with the memory error by sending an eject event to
15 the operating system.